

MICROPROCESSOR AND INTERFACING LAB

# Post Lab questions exp-4

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Final Grade  
Submitted 3/3/22, 9:54 PM (UTC+5:30) 5 / 5

Question scores appear after all grades are posted

Assignment Content

### Question 1

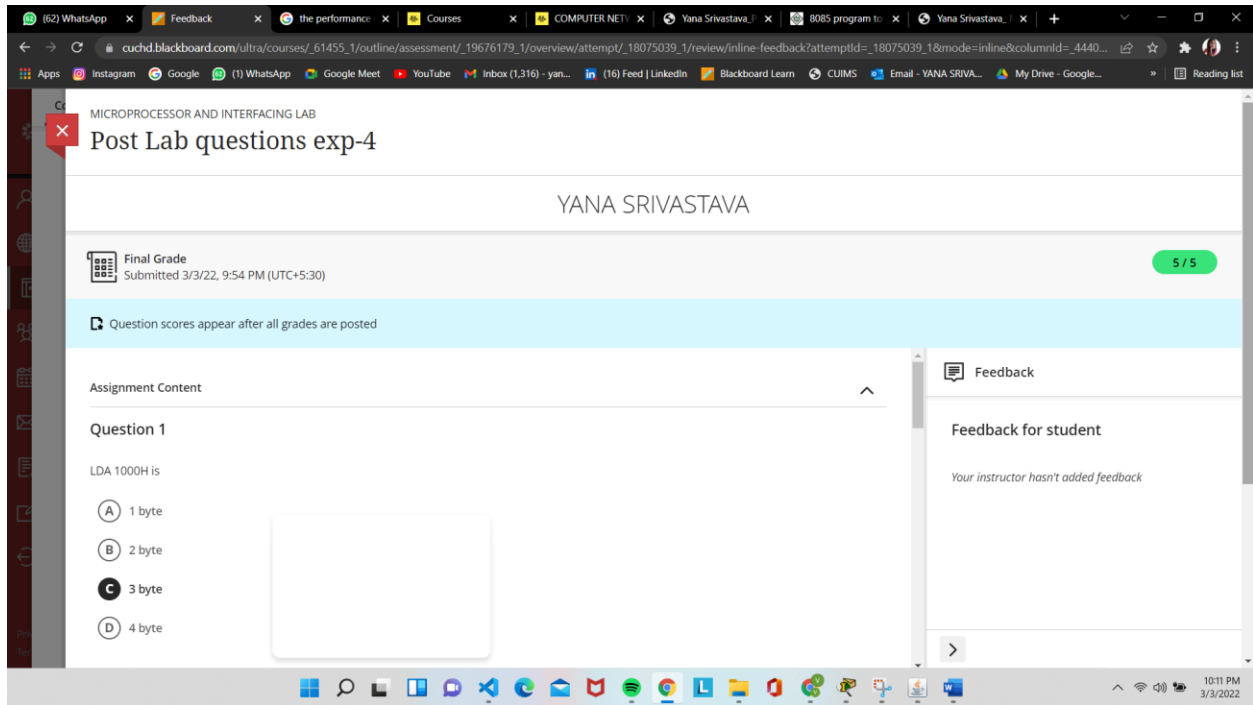
LDA 1000H is

- A 1 byte
- B 2 byte
- C 3 byte
- D 4 byte

Feedback

#### Feedback for student

Your instructor hasn't added feedback



### Question 2

CMA is equivalent to

- A AND gate
- B OR gate
- C NOT gate
- D NAND gate

### Question 3

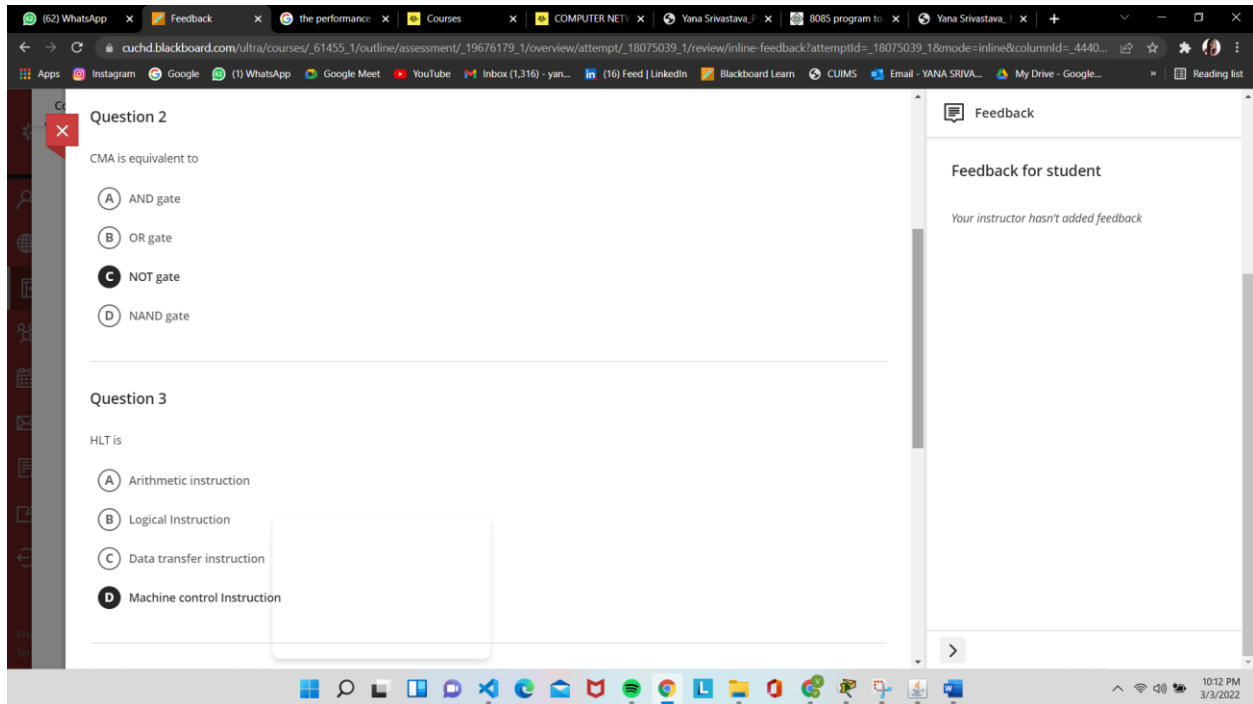
HLT is

- A Arithmetic instruction
- B Logical Instruction
- C Data transfer instruction
- D Machine control Instruction

Feedback

#### Feedback for student

Your instructor hasn't added feedback



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**Question 4**

If Accumulator= 10101010 then the value after CMA  
CMA  
ADI 01H  
is equal to

(A) 10101010  
 (B) 10101011  
 (C) 11001101  
 (D) 00010001

**Question 5**

For applying CMA on a 8 bit number, the number need to be stored in Accumulator

(T) True  
 (F) False

**Feedback**

Feedback for student

Your instructor hasn't added feedback

10:13 PM  
3/3/2022